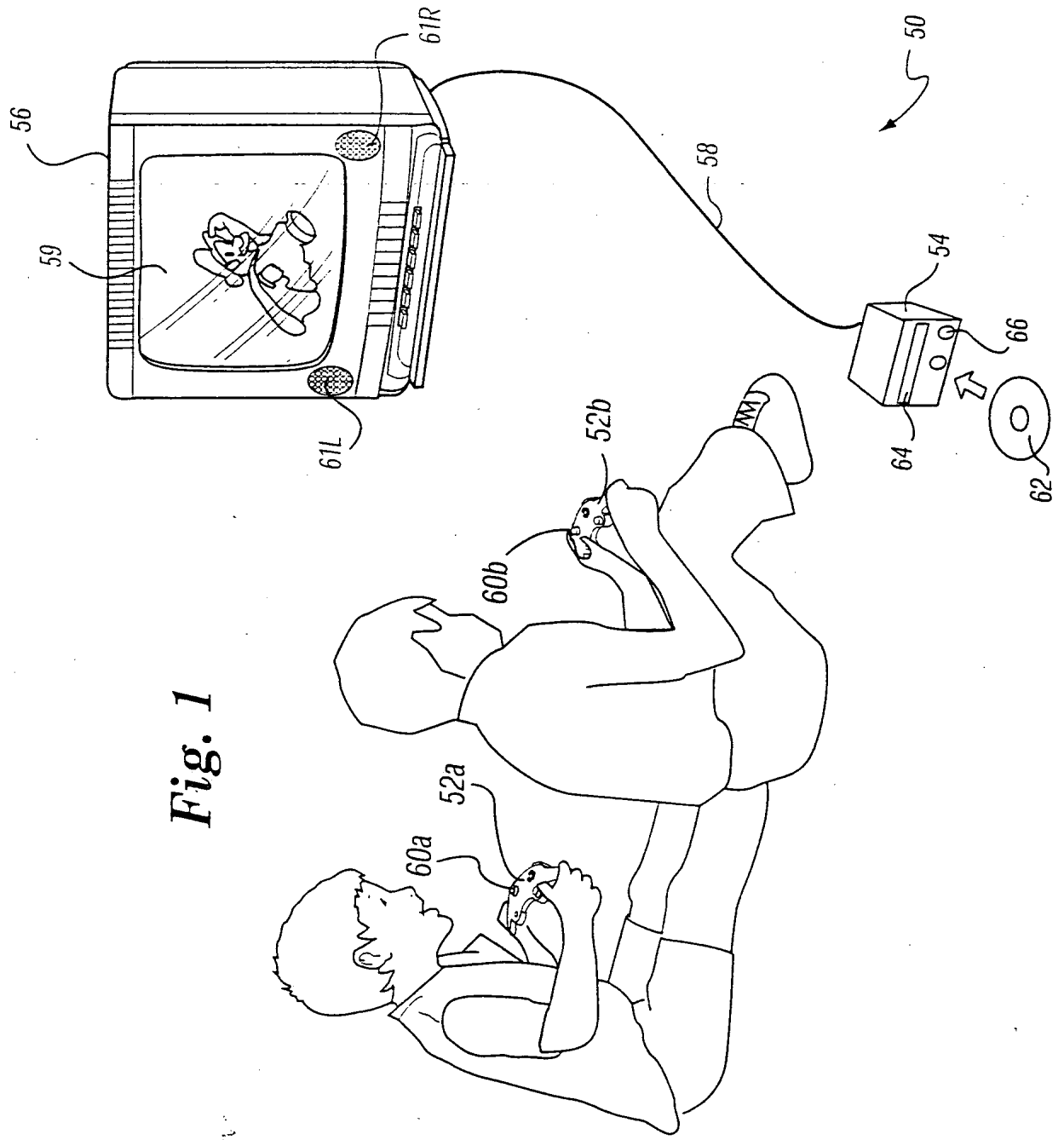


Fig. 1



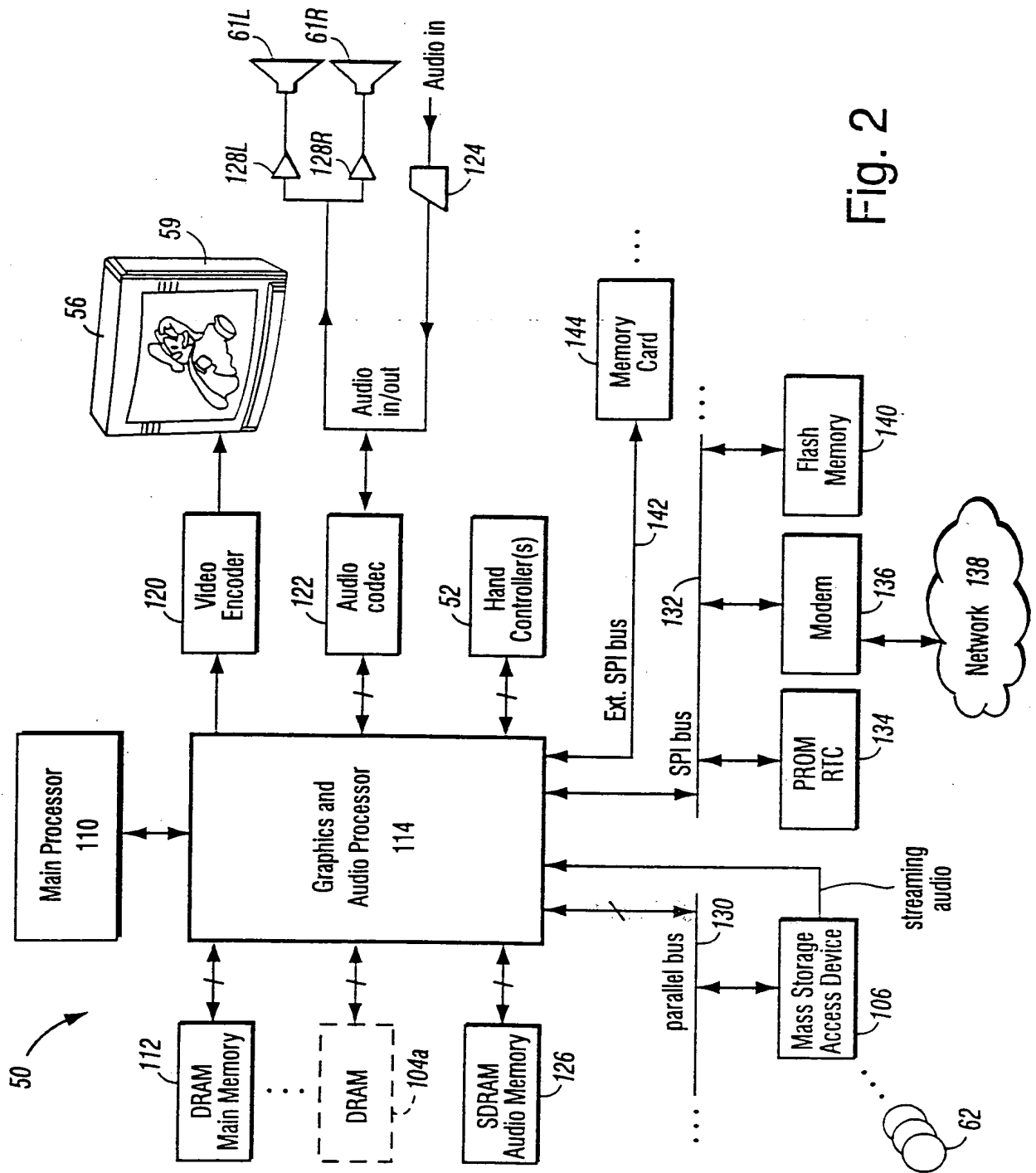
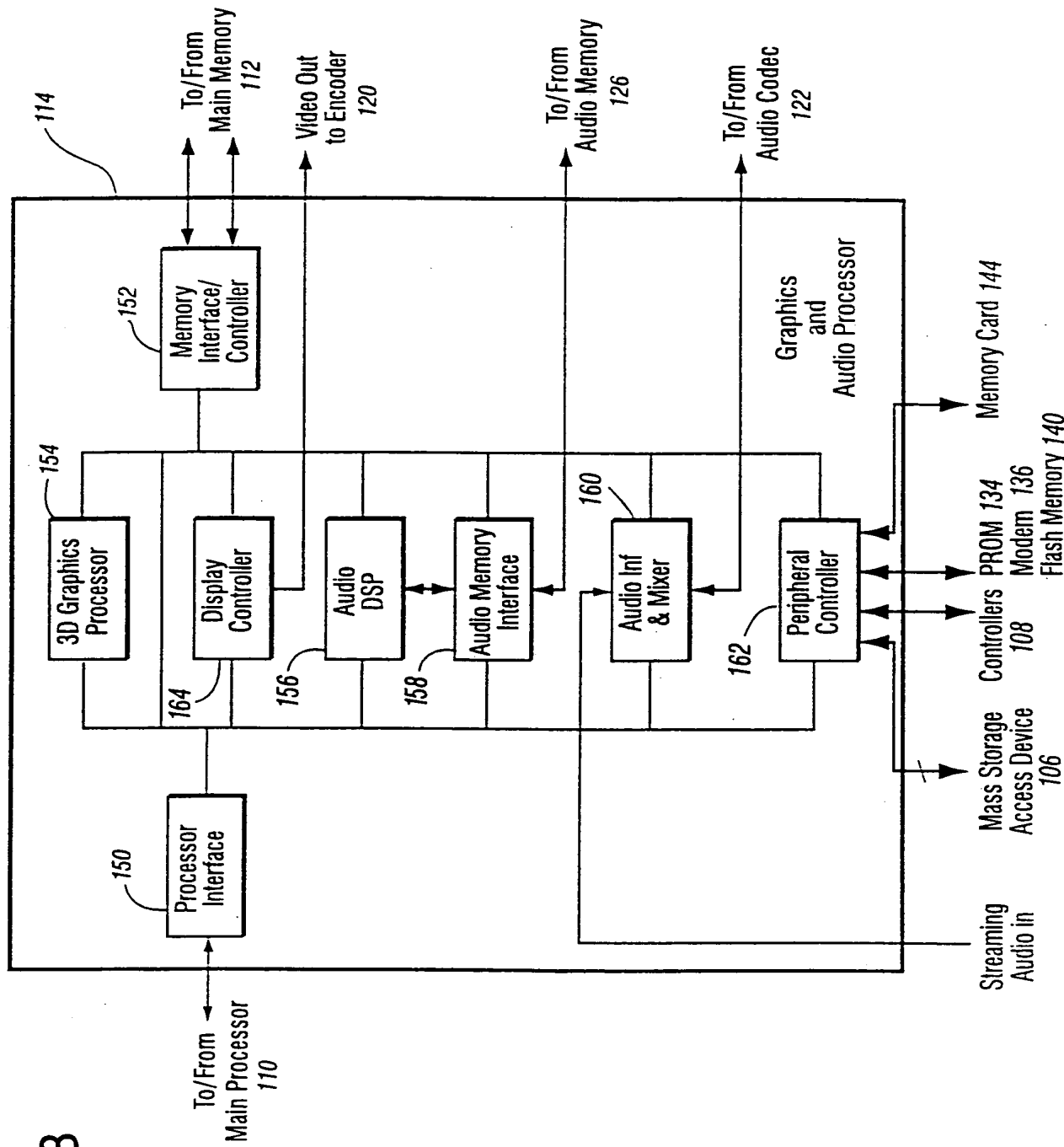


Fig. 2

Fig. 3



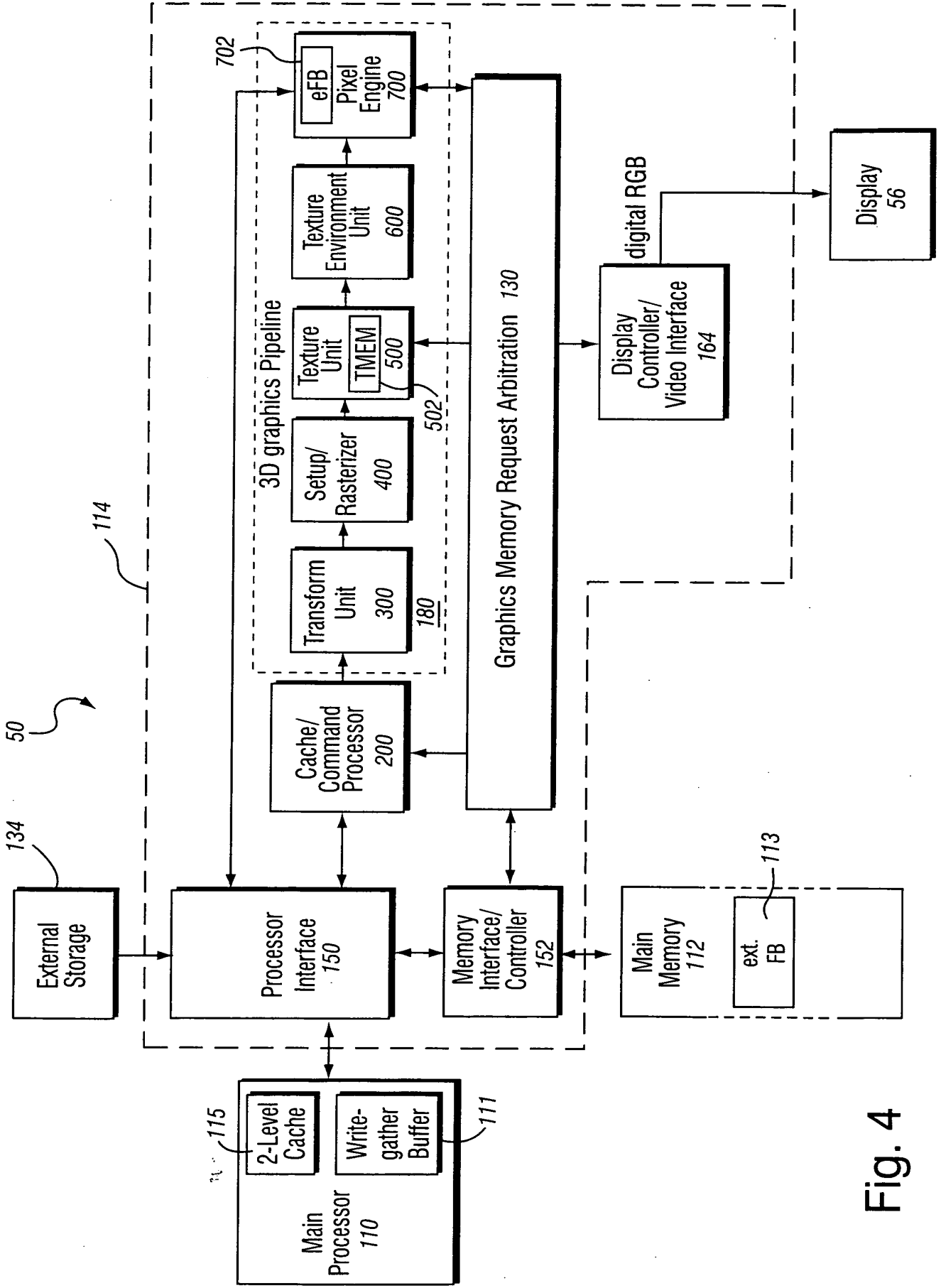


Fig. 4

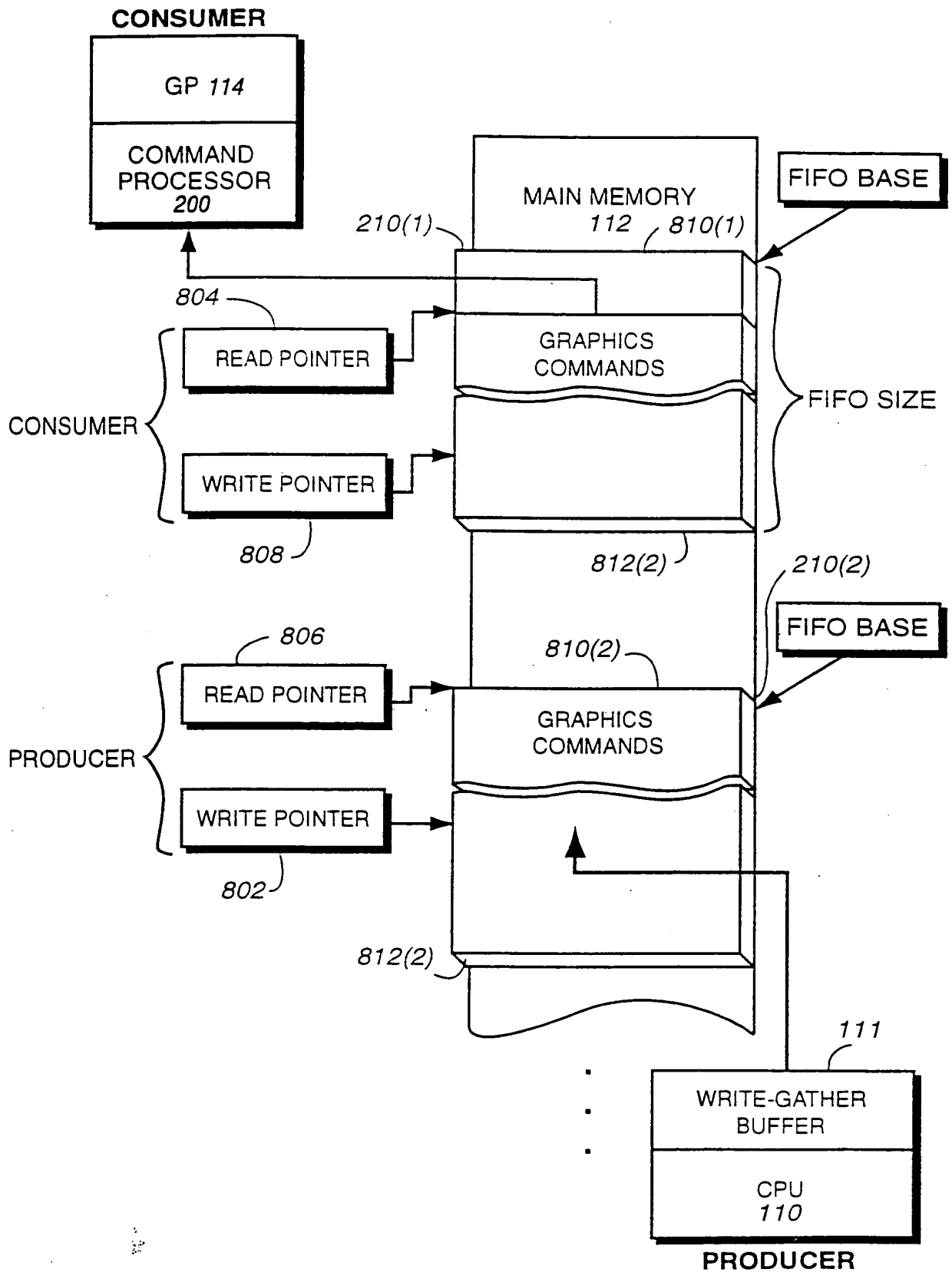


Fig. 6 EXAMPLE MULTI - BUFFERING

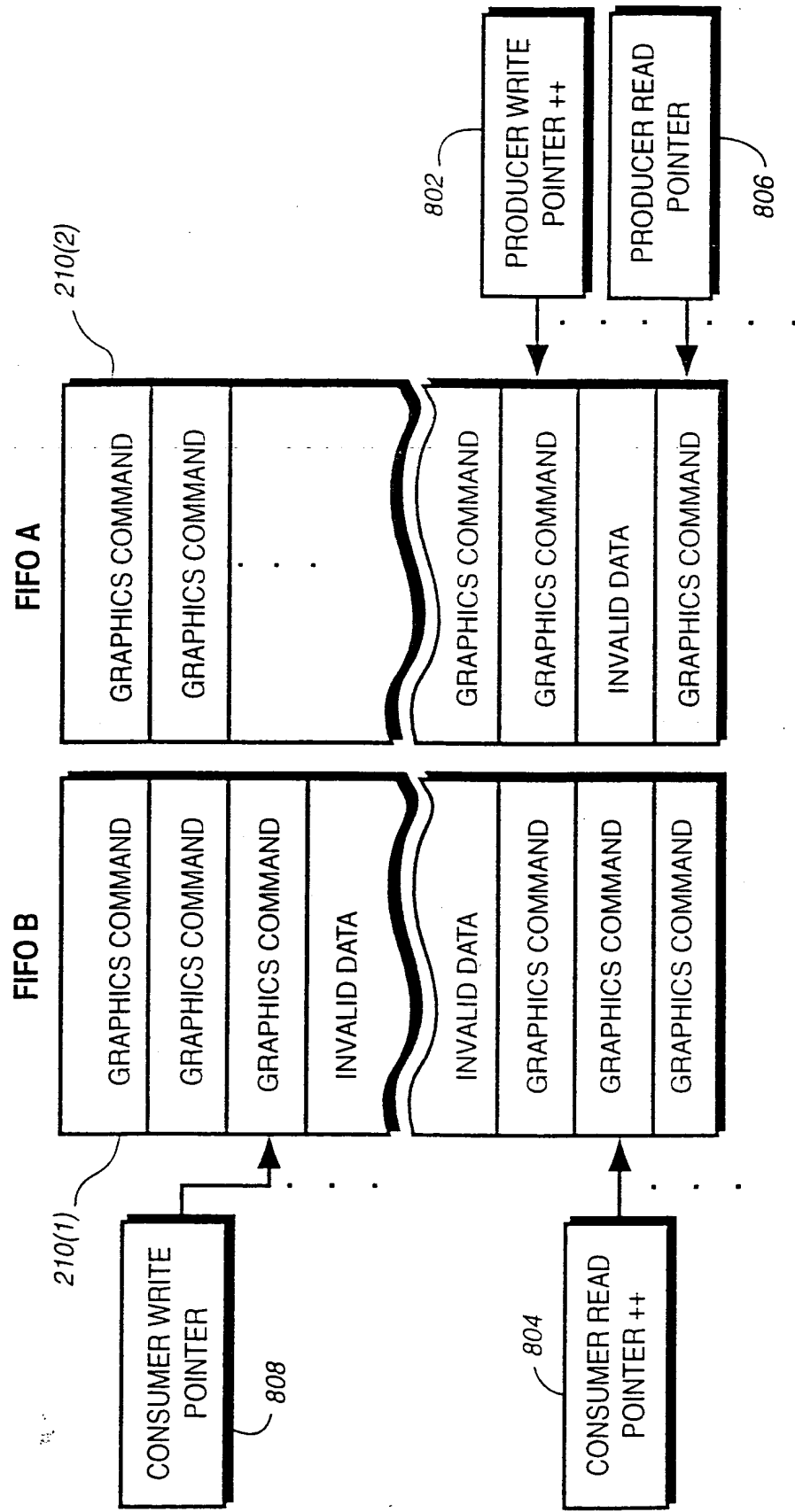


Fig. 7 EXAMPLE INDEPENDENT CONSUMER AND PRODUCER
READ AND WRITE POINTERS

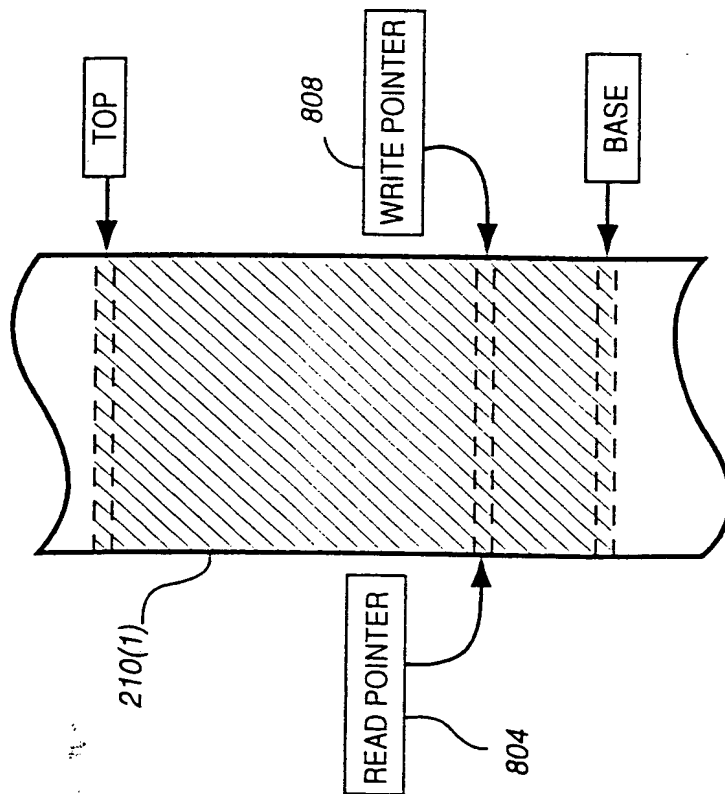


Fig. 8A EMPTY CONDITION

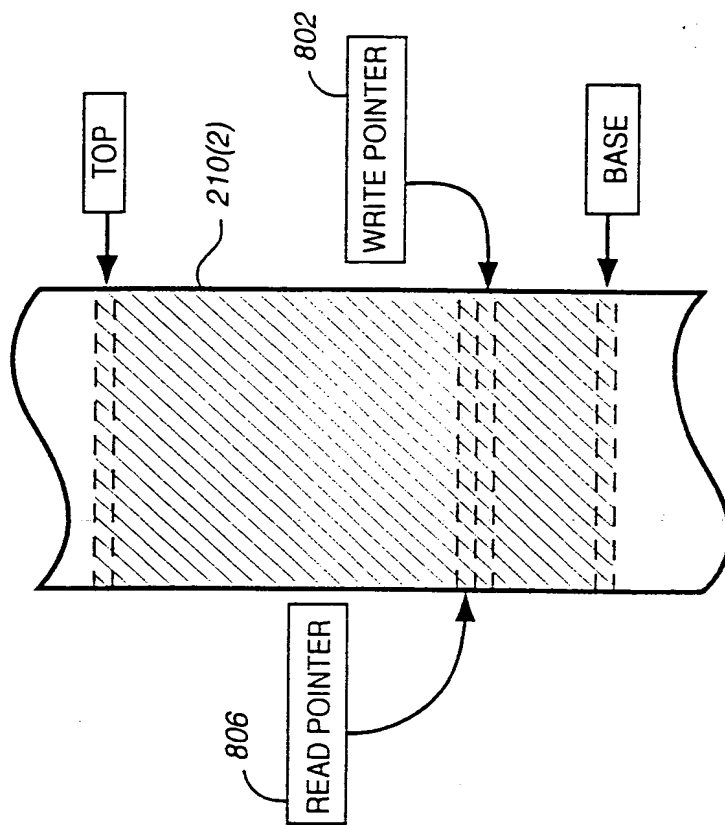


Fig. 8B FULL CONDITION

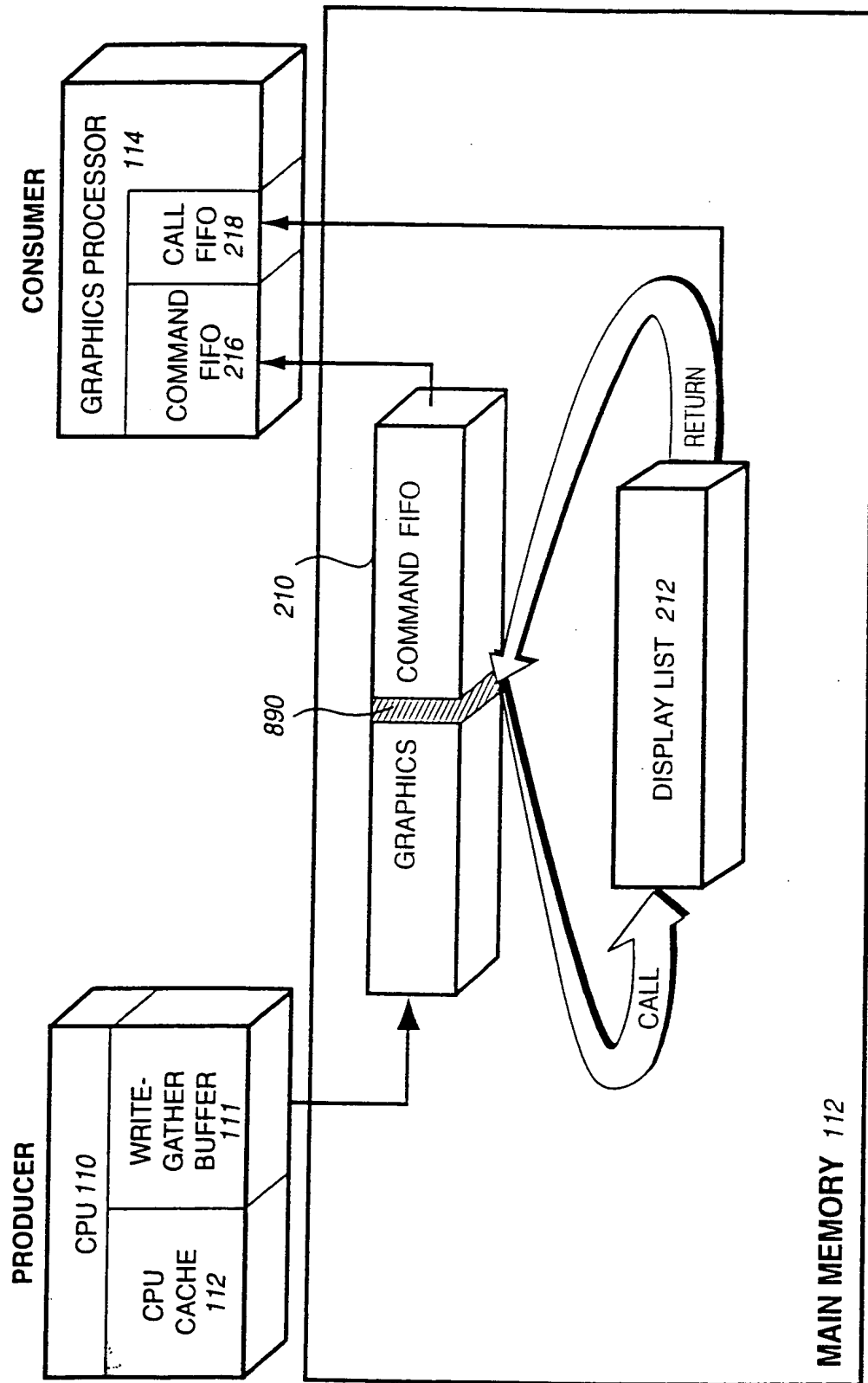


Fig. 9 EXAMPLE DISPLAY LIST CALL FROM COMMAND FIFO

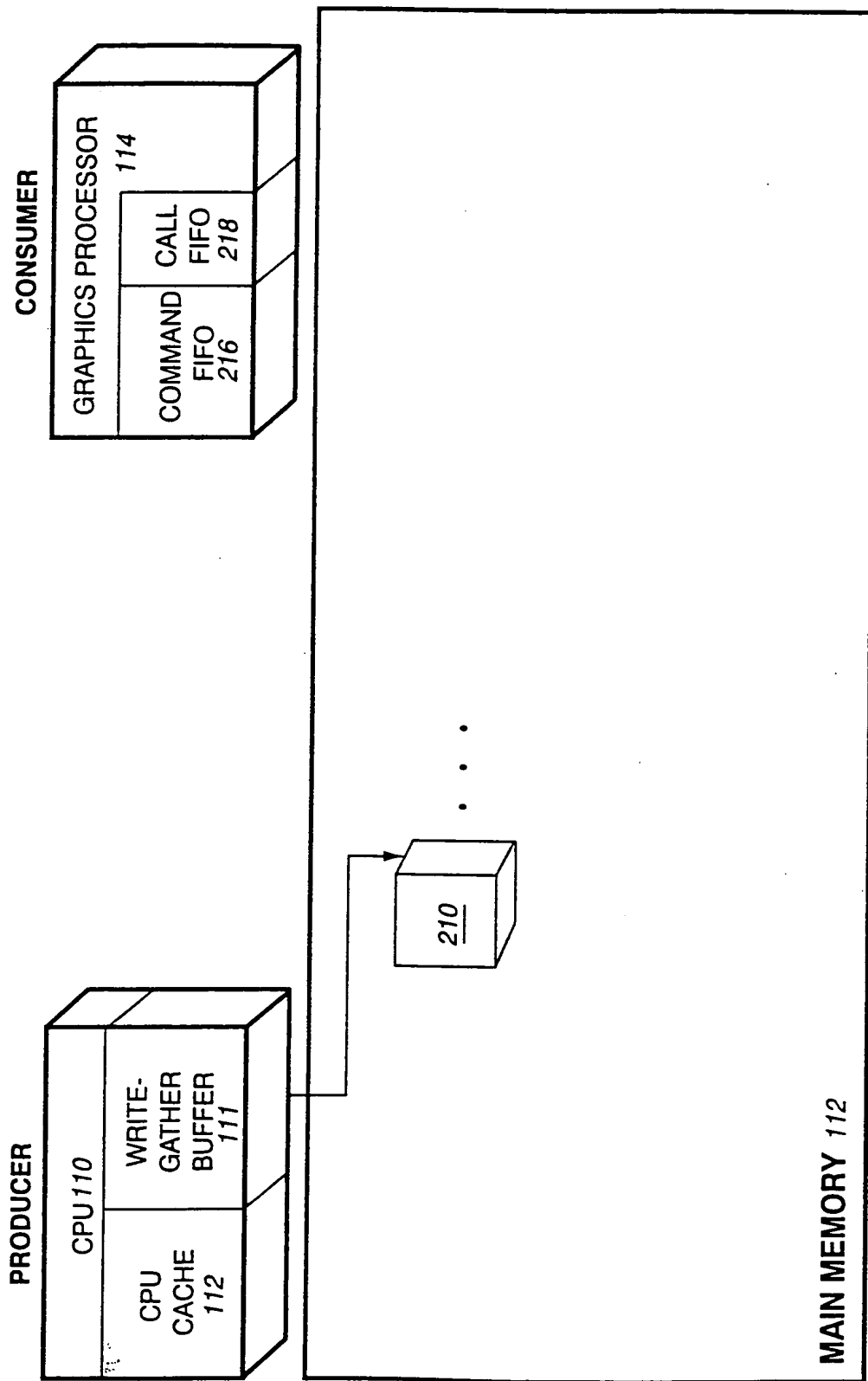


Fig. 10A EXAMPLE WRITE TO FIFO BUFFER

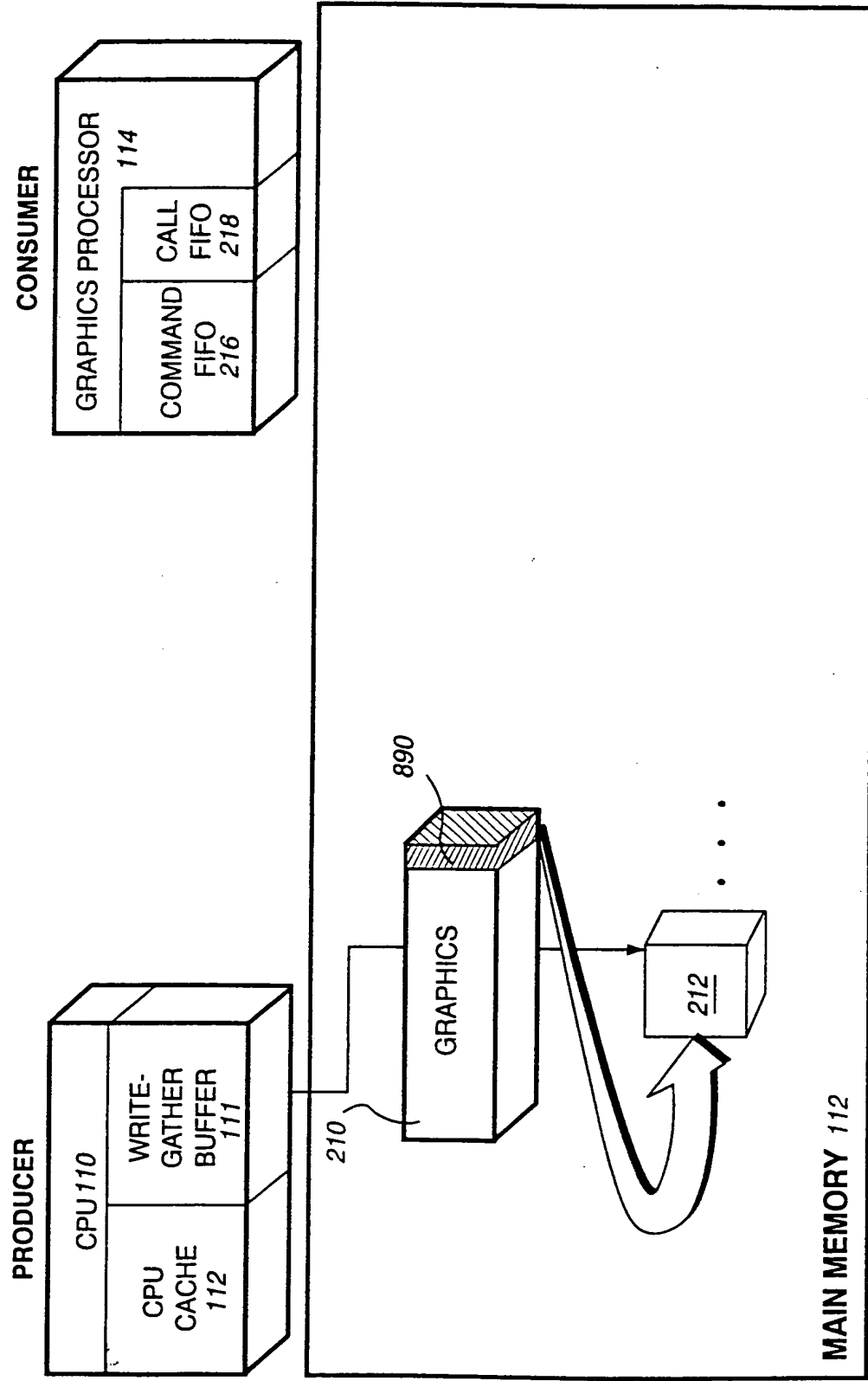


Fig. 10B EXAMPLE BEGIN DISPLAY LIST COMMAND

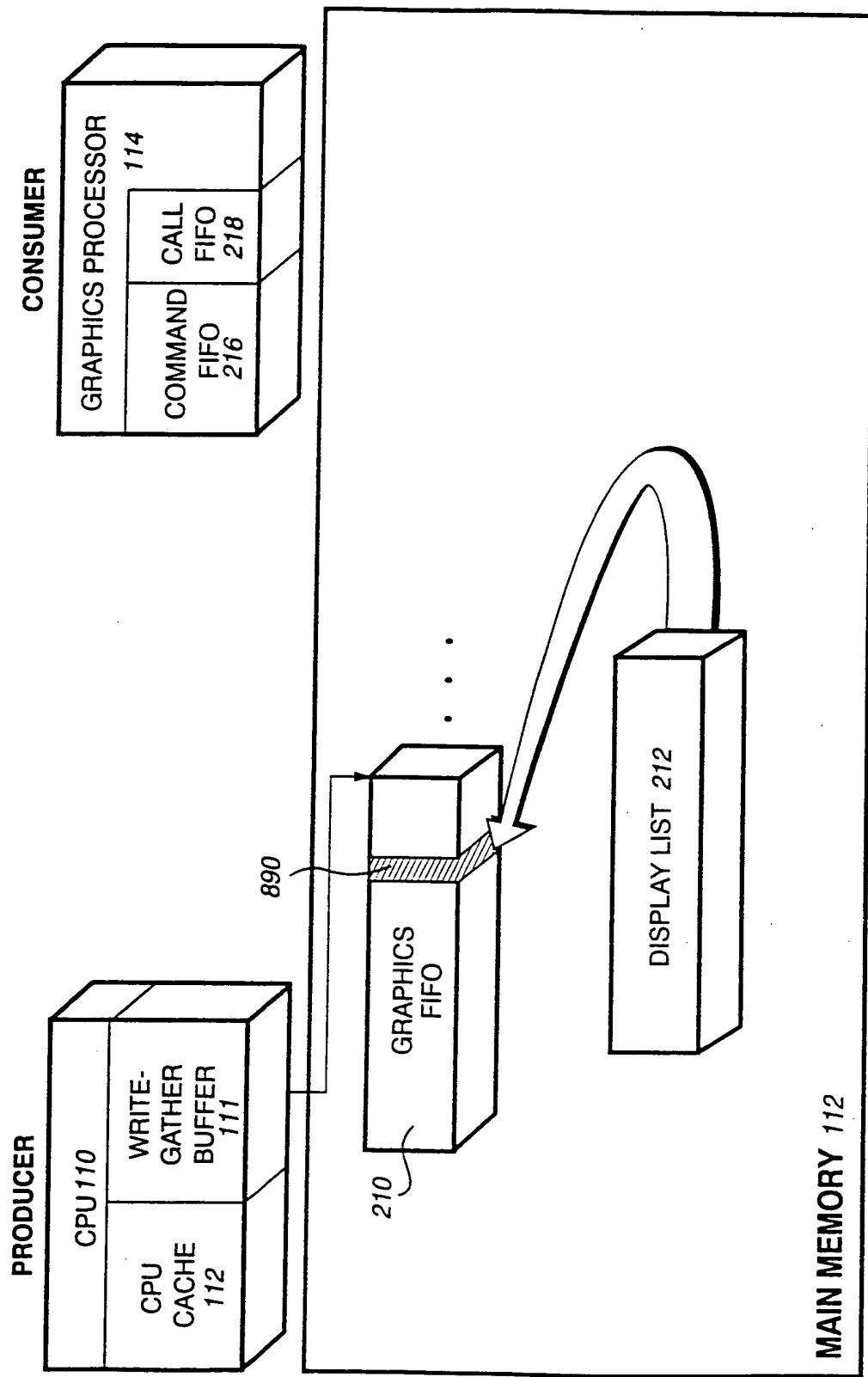


Fig. 10C EXAMPLE END DISPLAY LIST COMMAND

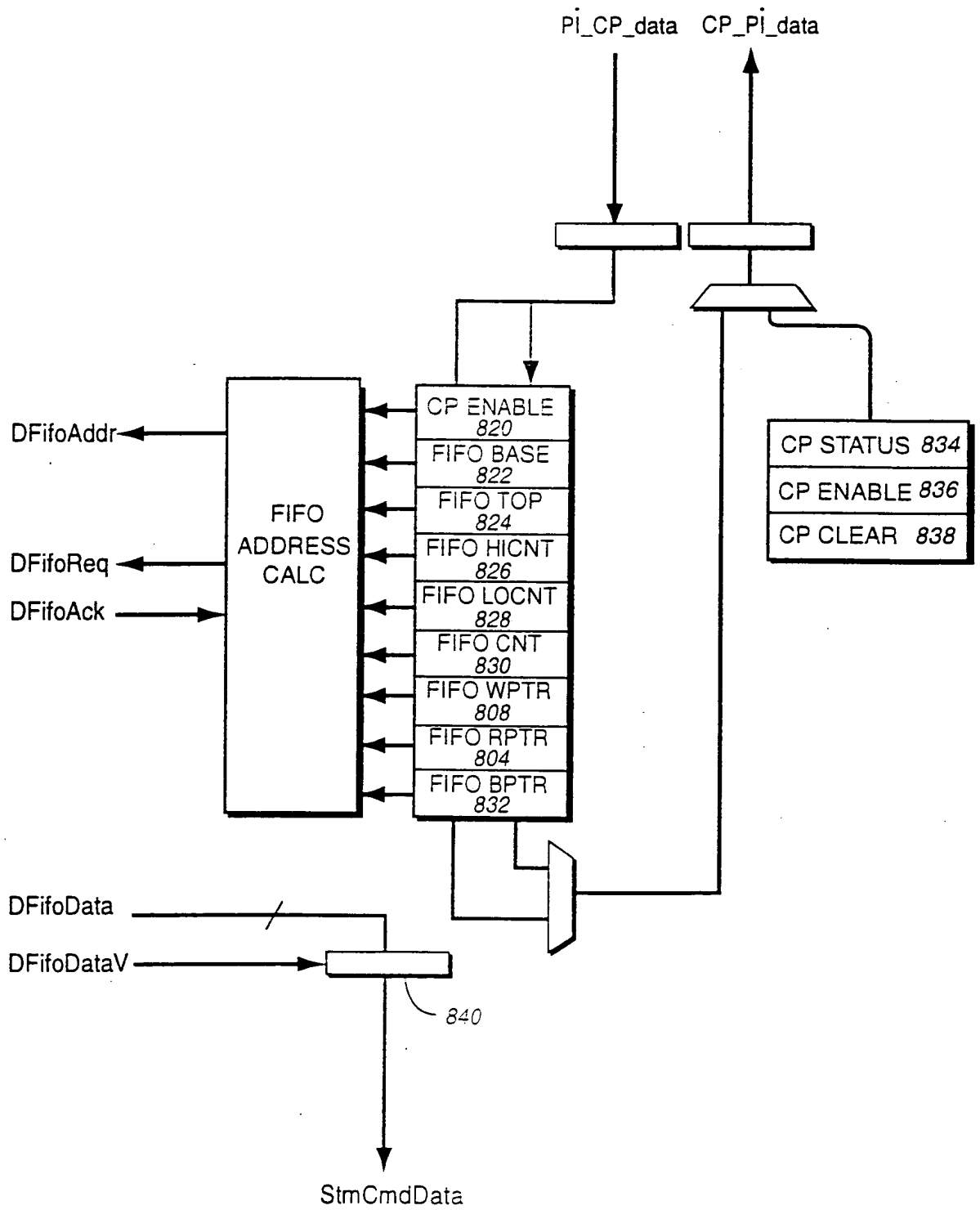


Fig. 11 EXAMPLE FIFO MANAGER

FIG. 12A is a block diagram of a system architecture for emulating software on a host platform.

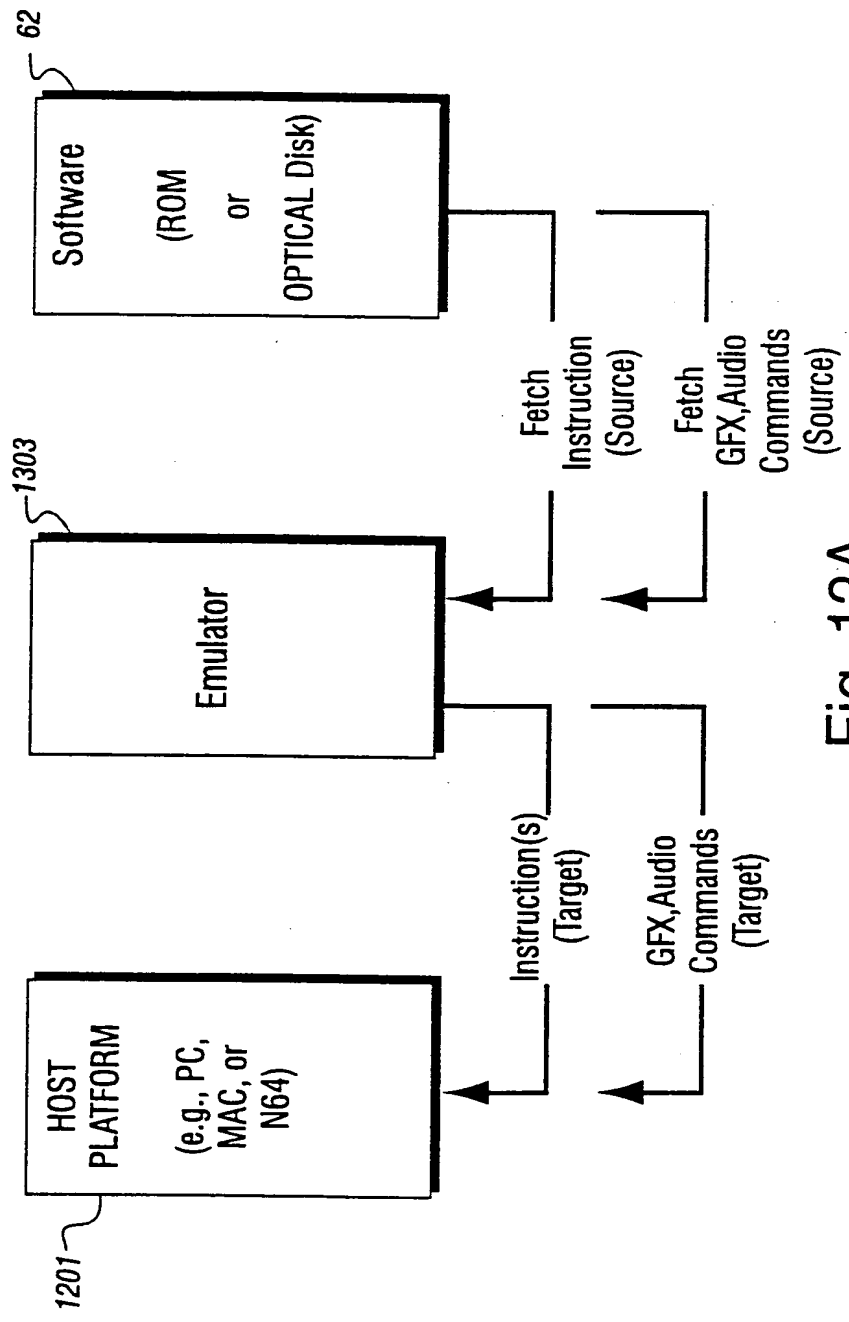


Fig. 12A

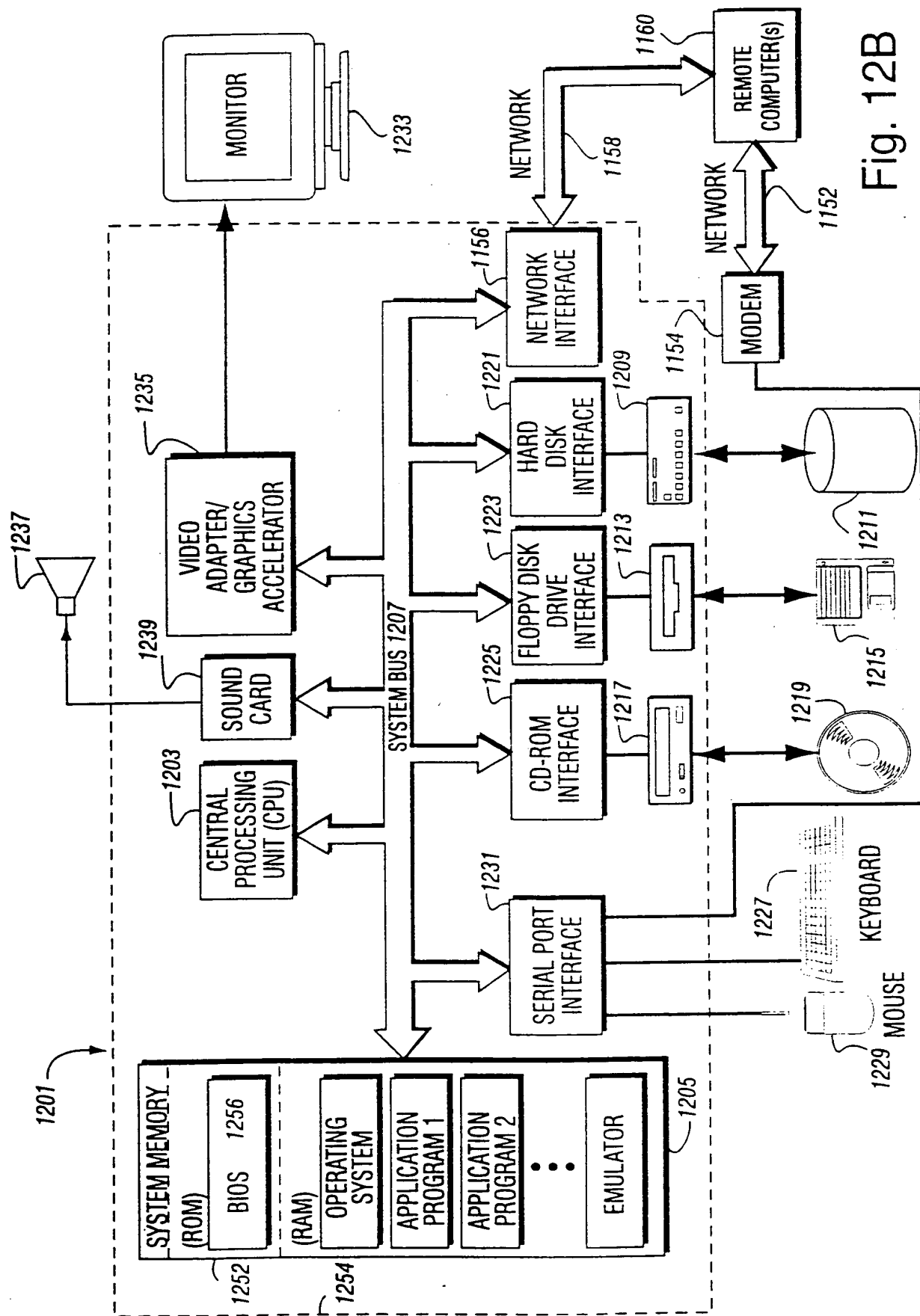


Fig. 12B